RIGOL

Declassification Guide

Spectrum Analyzer

May 2012

RIGOL Technologies, Inc.

DSA800 Series

DSA800 series spectrum analyzer consists of DSA815.

Instrument Memory

This section contains information on the types of memory available in your instrument. It explains the size of memory, how it is used, its location, volatility and the clearing procedure.
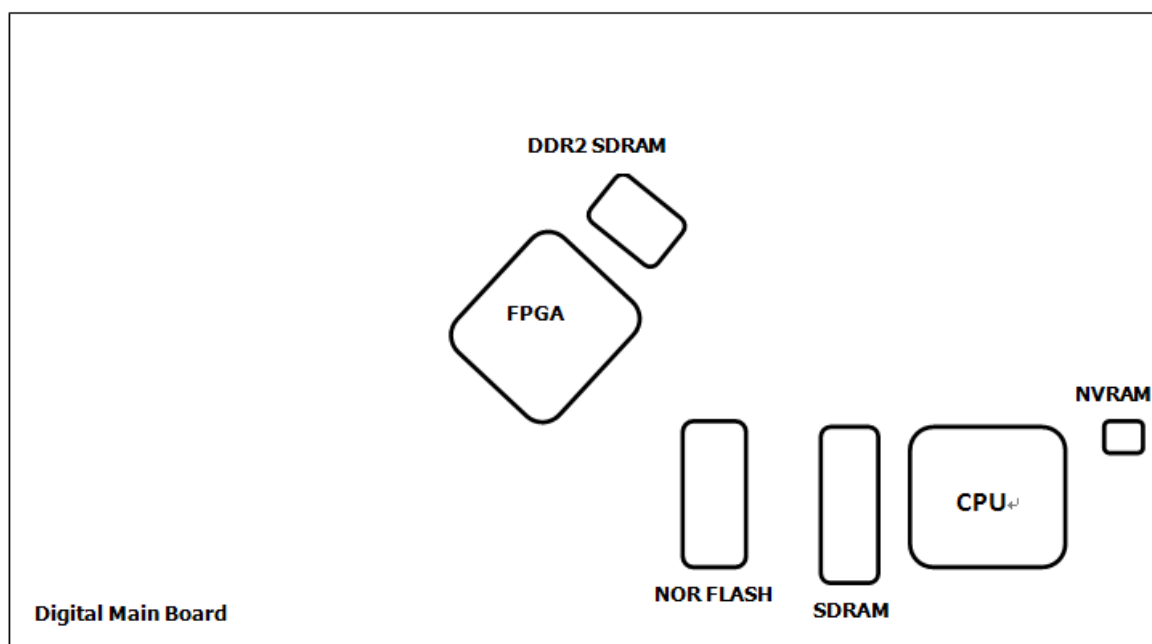
Instrument memory:

Memory type and size	Writable during normal operation	Data retained when powered off	Purpose/ contents	Data input method	Location in instrument and remarks	Sanitization procedure
Main Memory (SDRAM) 32MB	Yes	No	Operation	Operation system	Main board in CPU area	Cycle power
Display Memory (DDR2 SDRAM) 64MB	Yes	No	Display screen data	Display controller	Main board in FPGA area	Cycle power
Main Memory (FLASH) 8MB	Yes	Yes	Firmware/ Calibration data/ System data/User data	System update/ Calibration /First leaving factory/ User storage operation	Main board in CPU area	Clear the user data
NVRAM 2kB	Yes	Yes	System setting	Operation system	Main board in CPU area	Default all settings

NOR FLASH:

Partition size	Writable during normal operation	Data retained when powered off	Purpose/ contents	Data input method	Location in instrument and remarks	Sanitization procedure
3.4375MB	No	Yes	Firmware	System update	Main board in CPU area	No need
64kB	No	Yes	Calibration data	Calibration	Main board in CPU area	No need
128kB	No	Yes	System data	First leaving factory	Main board in CPU area	No need
4.375MB	Yes	Yes	User data	User storage operation	Main board in CPU area	Clear the user data

Position of Instrument Memory on Main Board

DSA1000&DSA1000A Series

DSA1000&DSA1000A series spectrum analyzer consists of DSA1030A, DSA1030 and DSA1020.

Instrument Memory

This section contains information on the types of memory available in your instrument. It explains the size of memory, how it is used, its location, volatility and the clearing procedure.

Instrument memory:

Memory type and size	Writable during normal operation	Data retained when powered off	Purpose/ contents	Data input method	Location in instrument and remarks	Sanitization procedure
Main Memory (SDRAM) 32MB	Yes	No	Operation	Operation system	Main board in CPU area	Cycle power
Main Memory (NOR FLASH) 4MB	Yes	Yes	Firmware/ Calibration data/ System data	System update/ Calibration /First leaving factory	Main board in CPU area	Clear the user data
Display Memory (DDR2 SDRAM) 64MB	Yes	No	Display screen data	Display controller	Main board in FPGA area	Cycle power
Data Memory (NAND FLASH) 2GB	Yes	Yes	User data	User storage operation	Main board in CPU area	Clear the user data
NVRAM 2kB	Yes	Yes	System setting	Operation system	Main board in CPU area	Default all settings

NOR FLASH:

Partition size	Writable during normal operation	Data retained when powered off	Purpose/ contents	Data input method	Location in instrument and remarks	Sanitization procedure
3.5625 MB	No	Yes	Firmware	System update	Main board in CPU area	No need
192kB	No	Yes	Calibration data	Calibration	Main board in CPU area	No need
132kB	No	Yes	System data	First leaving factory	Main board in CPU area	No need
64kB	Yes	Yes	User data	User storage operation	Main board in CPU area	Clear the user data
60kB	Yes	Yes	LXI information	Operation system	Main board in CPU area	Clear the user data

Position of Instrument Memory on Main Board